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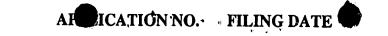
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TITLE

50440MD1406	00/611 000		
50449/BP1406	09/611,809	July 7, 2000	Encryption Processor
		i	for Performing
			Accelerated
			Computation to
			Establish Secure
			network Sessions
			Connections
50454/BP1421	09/827,882	April 4, 2001	Authentication engine
			Architecture and
			Method
50464/BP1567.3	09/859,330	May 16, 2001	Methods and
		1.	Apparatus for
			Hardware
			Normalization and
			Denormalization
50468/BP1688.1	09/892,240	June 26, 2001	Methods and
			Apparatus for
		•	Implementing a
			Cryptography Engine
50472/BP1688.3	09/892,242	June 26, 2001	Methods and
			Apparatus for
			Implementing a
			Cryptography Engine
50470/BP1688.2	09/892,310	June 26, 2001	Methods and
	ir.		Apparatus for
			Implementing a
	*	<u> </u>	Cryptography Engine
50462/BP1567.2	09/916,557	July 26, 2001	Stream Cipher
			Encryption
			Application
			Accelerator and
			Methods Thereof
50460/BP1567.1	09/929,178	August 14, 2001	E-commerce Security
	,		Processor Alignment
			Logic
50466/BP1567.4	09/948,203	September 6, 2001	Methods and
			Apparatus for
			Implementing a
			Cryptography Engine
50479/BP2054	10/004,458	October 23, 2001	Methods and
			Apparatus for
			Collapsing Interrupts

50456 PR0000	1.010.01.00	<b>***</b>	
50476/BP2009	10/026,109	December 20, 2001	Methods and
			Apparatus for
			Accelerating ARC4
			Processing
50456/BP1687	10/042,019	January 8, 2002	Fast SHA1
			Implementation
50487/BP2280	10/160,143	May 31, 2002	Methods and
			Apparatus for
			Accelerating Secure
			Session Processing
50488/BP2281	10/160,322	May 31, 2002	Methods and
			Apparatus for
		İ	performing
			Encryption and
			Authentication
50484/BP2278	10/160,335	May 31, 2002	Methods and
			Apparatus for
,			Performing
			Authentication and
		•	Decryption
50486/BP2279	10/161,475	May 31, 2002	Data Transfer
			Efficiency in a
			Cryptography
			Accelerator System
50443/BP1405CON	10/227,491	August 23, 2002	Security Chip
			Architecture and
		· .	Implementations for
	1		Cryptography
			Acceleration
50482/BP2093	10/330,694	December 24, 2002	Methods and
			Apparatus for
			Performing Hash
			Operations in a
		·	Cryptography
			Accelerator
50501/BP2410	10/350,902	January 23, 2003	Cryptography
			Accelerator Interface
		}	Decoupling from
			Cryptography
			Processing Cores
50499/BP2409	10/350,922	January 23, 2003	Cryptography
			Accelerator Data
			Routing Unit

CPH/BP NO.





50495/BP2404	10/351,258	January 23, 2003	Methods and Apparatus for Ordering Data in a Cryptography Accelerator
50490/BP2406	10/378,035	February 27, 2003	Methods and Apparatus for Initialization Vector Processing

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